2002 SCHEME

EE841

## Eighth Semester B.E. Degree Examination, June-July 2009 Embedded Systems

Time: 3 hrs. Max. Marks:100

Time, 5 ms.		Marks:100	
		Note: Answer any FIVE full questions.	
1	a. b.	What is an embedded system? Explain different types of embedded systems.  With a neat block diagram explain the unique and specific features of microcon	(08 Marks) troller 8051
	65.00	architecture.	(12 Marks)
2	<u>a</u> .	Explain queue and stack data structures with their application.	(08 Marks)
	ņ.	Explain round robin and round robin with interrupts architectures. Also mention and disadvantages of both architectures.	(12 Marks)
3	3.	What are the advantages of high level language coding?	(06 Marks)
	5.	Explain different states of a task in RTOS.	(06 Marks)
	2.	Give a comparison of three methods of protecting shared data.	(08 Marks)
4	1	Explain briefly various issues for selecting a DAC.	(10 Marks)
	5.	With a neat diagram and necessary waveforms explain 8-bit ramp ADC.	(10 Marks)
5		Define i) Resolution ii) Reproducibility iii) Precision iv) Statistical of	
		with respect to an instrument.	(08 Marks)
	Ь.	With a neat block diagram explain general instrumentation / control system.	(12 Marks)
6		With a neat block diagram explain half duplex and full duplex communication.	(06 Marks)
	ь.	With necessary waveforms and diagrams explain switch bouncing. Also explain	
		be removed using a capacitor.	(10 Marks)
	=	Define i) Baud rate ii) Band width for serial communication.	(04 Marks)
7		Differentiate memory mapped I/O and isolated I/O computer architecture.	(06 Marks)
	5	Explain general approach to memory interfacing on a 6811 in expanded mode.	(06 Marks)
	-	Interface 32 K PROM to 6811 processor.	(08 Marks)
•	1	List out the advantages of hardware implementation and software implementation	
		subsystem.	(08 Marks)
	b.		
		i) ROM variants.	
		ii) SoC for cell-phone.	(12 Marks)

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